

**DECLARATION FOR PATENT APPLICATION**

As the below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name. I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: **A METHOD OF AND APPARATUS FOR INTEGRATING FLASH EPROM AND SRAM CELLS ON A COMMON SUBSTRATE**. The specification of which is attached hereto. I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claim

Yes No

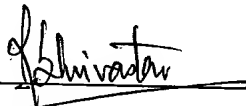
Number	Country	Day/Month/Year Filed

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status: Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Sole and First Inventor: Ritu Shrivastava

Inventor's Signature: 

5/19/99

Date

Residence: 44455 Parkmeadow Drive, Fremont, California 94539

Citizenship: United States

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PATENT  
Attorney Docket No.: ALSC-00300

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: ) Group:  
Ritu Shrivastava ) Art Unit:  
 ) Examiner:  
Serial No.: )  
 )  
Filed: herewith )  
 ) **POWER OF ATTORNEY BY ASSIGNEE**  
For: **A METHOD OF AND APPARATUS FOR** )  
 **INTEGRATING FLASH EPROM AND** )  
 **SRAM CELLS ON A COMMON** )  
 **SUBSTRATE** )

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

**Alliance Semiconductor**, Assignee of the above-identified application by Assignment dated \_\_\_\_\_ hereby appoints the members of the firm of HAVERSTOCK & OWENS LLP, a firm including Thomas B. Haverstock (Reg. No. 32,571), Jonathan O. Owens (Reg. No. 37,902), Derek J. Westberg (Reg. No. 40,872) and Richard H. Butt (Reg. No. 40,932), 260 Sheridan Avenue, Suite 420, Palo Alto, California 94306, telephone: (650) 833-0160, facsimile: (650) 833-0170, as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

Jonathan O. Owens  
HAVERSTOCK & OWENS LLP  
260 Sheridan Avenue, Suite 420  
Palo Alto, California 94306  
Telephone: (650) 833-0160  
Facsimile: (650) 833-0170

I hereby certify that the Assignment document filed with the application or filed subsequent to the filing date of the application, has been reviewed and I hereby certify that, to the best of my knowledge and belief, title is with **Alliance Semiconductor**.

Alliance Semiconductor

Dated: 5/18/99

By: N. Damodar Reddy  
Name: N. Damodar Reddy  
Title: President